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25226 7590 05/15/2007

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Carol Diez	(Depositor's name)
<i>Carol Diez</i>	(Signature)
9-6-07	(Date)

08/07/2007 FMETEK12 00000003 031952 10713753

01 FC:1501 1400.00 DA  
 02 FC:8001 18.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10713,753	11/13/2003	Bruce W. McCaughy	10585-019-999	1627

TITLE OF INVENTION: SYSTEM AND METHOD FOR SUPPORTING MULTI-RATE SIMULATION OF A CIRCUIT HAVING HIERARCHICAL DATA STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$0	\$1400	08/15/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
PIERRE LOUIS, ANDRE	2123	703-014000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2 \_\_\_\_\_  
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Cadence Design Systems, Inc.

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☒ Issue Fee  
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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date 8/6/2007

Typed or printed name Thomas C. Chan

Registration No. 51,543

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FROM: Thomas Chan  
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DATE: August 6, 2007

Number of pages with cover page:	3	
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Preparer of this slip has confirmed that facsimile number given is correct: **10475/cxd7**

**Comments:**

**Official Filing**

Docket No.: 188122002000  
Art Unit: 2123  
Examiner: A. Pierre-Louis  
U.S. Patent Application Serial No.: 10/713,753  
Filing Date: November 13, 2003  
Inventor(s): Bruce W. MCGAUGHY et al.  
Title: SYSTEM AND METHOD FOR SUPPORTING MULTI-RATE SIMULATION OF A  
CIRCUIT HAVING HIERARCHICAL DATA STRUCTURE

Papers enclosed herewith:

1. Part B- Fees Transmittal + duplicate copy for fee processing (2 pages)

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